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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/632,988	08/04/2003	Seong Ho Kang	YHK-0115	2974
34610	7590 02/23/2006		EXAMINER	
FLESHNER & KIM, LLP P.O. BOX 221200			BODDIE, WILLIAM	
CHANTILLY, VA 20153			ART UNIT	PAPER NUMBER
	•		2674	
			DATE MAILED: 02/23/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

•	Application No.	Applicant(s)					
	10/632,988	KANG ET AL.					
Office Action Summary	Examiner	Art Unit					
	William Boddie	2674					
- The MAILING DATE of this communication appears on the cover sheet with the correspondence address - Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be time will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nety filed the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 18 Ja	1) Responsive to communication(s) filed on <u>18 January 2006</u> .						
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3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4) Claim(s) 1,4-7,9-11,13-15 and 20-22 is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1,4-7,9-11,13-15 and 20-22</u> is/are reje	ected.						
7) Claim(s) is/are objected to.	7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examine	ır.						
10)⊠ The drawing(s) filed on <u>18 January 2006</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.							
Applicant may not request that any objection to the							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) ☐ All b) ☐ Some * c) ☐ None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s)							
1) Notice of References Cited (PTO-892)	4) Interview Summary						
Notice of Draftsperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail D 5) Notice of Informal F	ate Patent Application (PTO-152)					
Paper No(s)/Mail Date	6) Other:						

Application/Control Number: 10/632,988 Page 2

Art Unit: 2674

## **DETAILED ACTION**

1. In an amendment dated January 18<sup>th</sup>, 2006, the Applicant amended claims 1, 4-7, 9-11, and 13-14. Claims 2-3, 8, 12 and 16-19 were cancelled and new claims 20-22 were added. Currently claims 1, 4-7, 9-11, 13-15 and 20-22 are currently pending.

### **Drawings**

2. The drawings have been corrected to correct the misspelling of 'temperature', as such the previous drawing objection is withdrawn.

#### Specification

3. The title of the invention has been amended to sufficiently describe the invention, therefore the previous specification objection is withdrawn.

#### Response to Arguments

4. Applicant's arguments with respect to claims 1, 4-7, 9-11, 13-15 and 20-22 have been considered but are most in view of the new ground(s) of rejection.

# Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 1, 4-7, 9-10 and 20-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Awamoto et al. (US 6,720,940) in view of applicant's admitted prior art (figures 3 and 5, hereinafter referred to as APA).

Art Unit: 2629

With respect to claim 1, Awamoto discloses, a method of driving a plasma display panel using frames, each frame divided into a plurality of sub-fields, comprising (fig. 6):

applying a first driving waveform to said sub-fields at a temperature at a first prescribed temperature (T1'+Ti1 ... T8'+Ti8 in fig. 6); and

applying a second driving waveform different from the first driving waveform to said sub-fields at a second prescribed temperature, the first and second prescribed temperature being different (T1' ... T2' in fig. 6; also note col. 8, lines 16-24),

wherein each of said sub-fields includes a plurality of periods (TR,TA,TS in fig. 12), one of the periods being an initialization period (TR in fig. 12), which is divided into a set-up interval for forming wall charges at a discharge cell (Prx in fig. 12) and a set-down interval for erasing a portion of the wall charges formed in the set-up interval (col. 2, lines 25-28).

Awamoto does not expressly disclose, that the set-up interval waveforms are different from each other.

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are admitted prior art).

Awamoto and APA are analogous art because they are both from the same field of endeavor namely, driving waveforms for plasma displays.

Art Unit: 2674

At the time of the invention it would have been obvious to apply a different set-up interval, as taught by APA, to the set-up intervals of Awamoto. To further explain, Awamoto discloses, applying different waveforms based on the panel temperature. Applicant admits prior art for two different set-up waveforms. One waveform, while improving contrast, causes brightness misfires during certain temperatures. It seems obvious that one of ordinary skill in the art at the time would have thought to also alter the set-up waveforms of Awamoto depending on the panel temperature.

The motivation for doing so would have been to improve the contrast of the display (APA, para. 20) and to reduce brightness misfires (APA, para. 33).

Therefore it would have been obvious to combine APA with Awamoto for the benefit of improved contrast to obtain the invention as specified in claim 1.

With respect to claim 4, Awamoto and APA disclose, the method as claimed in claim 1 (see above).

While Awamoto does not expressly disclose, the steps of:

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval when said first driving waveform is supplied;

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell in the first half of the set-up interval; and floating the sustain electrode in the second half of the set-up interval.

APA discloses such a waveform in figure 5, with rising ramp (Ramp-up) and ground voltage and floating (Z set-up period).

Art Unit: 2674

Therefore it would have been obvious to replace the waveforms of Awamoto with the driving waveform of APA for the benefit of improved contrast (APA, para. 20) to obtain the invention as specified in claim 4.

With respect to claim 5, Awamoto and APA disclose, the method as claimed in claim 1 (see above).

While Awamoto does not expressly disclose, the steps of:

Applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval when said second driving waveform is supplied and

Applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell.

APA discloses such a waveform in figure 3, with rising ramp (Ramp-up) and ground voltage (Z set-up period).

Therefore it would have been obvious to replace the waveforms of Awamoto with the driving waveform of APA for the benefit of reduction of brightness misfires (APA, para. 33) to obtain the invention as specified in claim 5.

With respect to claim 6, Awamoto and APA disclose, the method as claimed in claim 1 (see above).

While Awamoto does not expressly disclose wherein said second prescribed temperature is within a range of temperature is 20° C. to –50° C, this further limitation is merely a design choice and would have been an obvious temperature range choice as this is approximately the range that a brightness misfire is likely to occur at when using conventional drive waveforms (APA, para. 33).

Art Unit: 2674

Therefore it would have been obvious to limit the low temperature to 20° C. to – 50° C to obtain the invention as specified in claim 6.

With respect to claim 7, Awamoto discloses, a method of driving a plasma display panel using frames, each frame being divided into a plurality of subfields, an initialization period included in each sub-field is divided into a set-up interval (Prx in fig .12) and a set-down interval for its driving (col. 2, lines 25-28), comprising the steps of displaying a picture on the panel (this is an inherent outcome of using the plasma display panel); sensing a driving temperature of the panel; and setting a driving waveform to be applied in the set-up interval in correspondence with said driving temperature of the panel (col. 8, lines 16-24),

wherein a first driving waveform supplied when said driving temperature of the panel is a first prescribed temperature is different form a second driving waveform supplied when said driving temperature of the panel is a second prescribed temperature, which is different from the first prescribed temperature (fig. 6, in which B and C are different frames caused by a sensed temperature; also note col. 8, lines 16-24), and

wherein each of said sub-fields includes a plurality of periods (TR,TA,TS in fig. 12), one of the periods being an initialization period (TR in fig. 12).

Awamoto does not expressly disclose, that specifically the set-up interval waveforms are different from each other.

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in

Art Unit: 2674

the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are prior art).

As noted above it would have been obvious to combine Awamoto with APA for the benefit of improved contrast to obtain the invention as specified in claim 7 (for further motivation and discussion see the rejection of claim 1).

With respect to claim 9, Awamoto and APA disclose, the method as claimed in claim 7 (see above), and altering the driving waveform in response to panel temperature.

Awamoto does not expressly disclose the steps of:

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval; and

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell during the set-up period.

APA discloses such a waveform in figure 3, with rising ramp (Ramp-up) and ground voltage (Z set-up period).

Therefore it would have been obvious to replace the waveforms of Awamoto with the driving waveform of APA for the benefit of reduction of brightness misfires (APA, para. 33) to obtain the invention as specified in claim 9.

With respect to clam 10, Awamoto discloses, the method as claimed in claim 8 (see above), and altering the driving waveform in response to panel temperature.

Awamoto does not expressly disclose the steps of:

Art Unit: 2674

applying a rising ramp waveform to a scan electrode provided at each discharge cell during the set-up interval; and

applying a ground voltage to a common sustain electrode provided, in parallel with the scan electrode, at each discharge cell; and

floating the sustain electrode in the second half of the set-up interval.

APA discloses such a waveform in figure 5, with rising ramp (Ramp-up) and ground voltage and floating (Z set-up period).

Therefore it would have been obvious to replace the waveforms of Awamoto with the driving waveform of APA for the benefit of improved contrast (APA, para. 20) to obtain the invention as specified in claim 10.

With respect to claims 20-21, the only additional limitation these claims present over their independent claims is that the first temperature is higher than the second temperature. Awamoto clearly states that one temperature range is higher than a preset value and one range is below that same preset value (col. 7, lines 3-8).

7. Claims 11, 13-15 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Awamoto et al. (US 6,720,940) in view of Nagai (US 6,011,355) and further in view of applicant's admitted prior art (figures 3 and 5, hereinafter referred to as APA).

With respect to claim 11, Awamoto discloses, a driving apparatus for a plasma display panel in which an initialization period included in each sub-field is divided into a set-up interval (Prx in fig. 12) and a set-down interval (remainder of TR in fig. 12) for its driving, comprising:

Art Unit: 2629

a temperature sensor for sensing a driving temperature of the panel (75 in fig. 3); a controller for controlling a turning-on and a turning-off of the switching device in correspondence with a temperature inputted from the temperature sensor (71,72 and 61 in fig. 3),

wherein said controller differently controls said turning-on and turning-off of an interval setting device when a driving temperature inputted from the temperature sensor is a first prescribed temperature and when a driving temperature inputted from the temperature sensor is a second prescribed temperature, the first and second temperatures being different (clear from operation of device, also see col. 7, lines 1-17).

Awamoto does not expressly disclose, a switching device provided between a plurality of common sustain electrodes provided at the panel and a ground voltage source, or that the set-up intervals are different from each other amongst the waveforms.

Nagai discloses, a switching device (28 in fig. 1) provided between a plurality of common sustain electrodes (X in fig. 1) provided at the panel and a ground voltage source.

APA discloses, wherein waveforms applied in the set-up interval of the first and second driving waveforms are different from each other while the waveforms applied in the other periods are substantially identical to each other (note the different set-up periods of figs. 3 and 5, both of which are prior art).

Nagai, APA and Awamoto are analogous art because they are all from the same field of endeavor namely, plasma displays driving methods.

Art Unit: 2674

At the time of the invention it would have been obvious to one of ordinary skill in the art to use the switching device, disclosed by Nagai, in the sustain driving circuitry of Awamoto, and then apply a different set-up interval, as taught by APA, to the set-up interval of Awamoto

The motivation for doing so would have been to hold the sustain electrodes at a ground level (Nagai, col. 12, lines 19-20) and thus reduce the chance of noise affecting the electrodes, and to improve the contrast (APA, para. 20).

Therefore it would have been obvious to combine Nagai, APA and Awamoto for the benefit of holding the electrodes at ground to obtain the invention as specified in claim 11.

With respect to claim 13, Awamoto and Nagai disclose, the driving apparatus as claimed in claim 12 (see above). They also disclose as shown above in claim 12 limitations, using the timing controller of Awamoto to control the switching device of Nagai.

While Awamoto and Nagai do not expressly disclose, floating the common sustain electrode when a driving temperature inputted from the temperature sensor is more than said low temperature.

As shown above APA discloses, such a waveform in figure 5, floating common sustain electrode (Z set-up period).

Therefore it would have been obvious to replace the more than low temperature waveform of Awamoto with the driving waveform of APA and using the switching device of Nagai to implement the waveform for the benefit of improved contrast (APA, para. 20)

Art Unit: 2674

and to hold the electrodes at ground when not driving the panel (Nagai, col. 12, lines 19-20) to obtain the invention as specified in claim 13.

With respect to claim 14, Awamoto, APA and Nagai disclose, the driving apparatus as claimed in claim 11(see above).

While Awamoto and Nagai do not expressly disclose, wherein said controller turns on the switching device during the set-up interval when a driving temperature inputted form the temperature sensor is said second prescribed temperature.

As shown above, APA disclose, such a waveform in figure 3, with ground voltage (Z set-up period).

Therefore it would have been obvious to replace the second prescribed temperature waveform of Awamoto with the driving waveform of APA and using the switching device of Nagai to implement the waveform for the benefit of reduction of brightness misfires (APA, para. 33) and to hold the electrodes at ground when not driving the panel (Nagai, col. 12, lines 19-20) to obtain the invention as specified in claim 14.

With respect to claim 15, Awamoto, APA and Nagai disclose, the driving apparatus as claimed in claim 11 (see above).

Awamoto further discloses:

a sustain driver for driving the common sustain electrode (66 in fig. 3);

a scan driver for driving a plurality of scan electrodes provided in parallel with the common sustain electrode (67 in fig. 3); and

a data driver for driving a plurality of address electrode provided in a direction crossing the common sustain electrode (68 in fig. 3),

wherein said timing controller controls the sustain drive and the scan driver and the data driver (note fig. 3 and col. 6, lines 37-41).

With respect to claim 22, the only additional limitation this claim presents over its independent claim is that the first temperature is higher than the second temperature. Awamoto clearly states that one temperature range is higher than a preset value and one range is below that same preset value (col. 7, lines 3-8).

#### Conclusion

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Application/Control Number: 10/632,988 Page 13

Art Unit: 2629

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Will Boddie whose telephone number is (571) 272-0666. The examiner can normally be reached on Monday through Friday, 7:30 - 4:00 EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Wlb 1/30/06

AMR A. AWAD
PRIMARY EXAMINER
AMY Alan Awa